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Win Xp Professional SP3 Home Edition win xp professional Home Edition Serial Key Download New Microsoft Xp Home Edition SP3 Key Windows XP Professional x64 Edition Service Pack 3 . Windows XP Home Edition Product Key" "for Microsoft Corporation Windows XP Home Edition" References Category:Excel Category:Windows security Category:Products introduced in 2001 Category:Microsoft Office1. Field of the Invention The present invention relates to a compound semiconductor device and a method of manufacturing the same. 2. Description of the Background Art Conventionally, a compound semiconductor device is formed by sequentially forming a gallium nitride (GaN) based compound semiconductor layer, an aluminum gallium nitride (AlGaN) based compound semiconductor layer and a gallium nitride (GaN) based compound semiconductor layer on a substrate, and growing a nitride based compound semiconductor layer thereon. More specifically, a first compound semiconductor layer is formed on the substrate, and a second compound semiconductor layer is formed thereon. Next, the first compound semiconductor layer is etched to be processed into a patterned shape. The etching treatment for the first compound semiconductor layer is performed using a gas containing chlorine (Cl) as a main component, to form an electrode terminal. When the first compound semiconductor layer is etched, a base portion of the compound semiconductor layer is also etched, and thus a portion of the substrate below the first compound semiconductor layer is etched. Particularly, when a gallium nitride based compound semiconductor layer is used as the first compound semiconductor layer, the compound semiconductor layer is easily etched at its base portion. Thus, the substrate is easily etched. The substrate is etched with a deeper depth and a wider width than the base portion of the first compound semiconductor layer, and an isolation pattern is etched with a deeper depth and a wider width than the base portion of the first compound semiconductor layer. The substrate is etched so as to form the isolation pattern, and then the isolation pattern is etched so as to be further processed into a desired shape. However, when the first compound semiconductor layer is etched so as to form the electrode terminal, a region in which the substrate is etched is also etched, and thus the isolation pattern cannot be formed. Thus, the compound semiconductor device cannot be formed. To avoid this, the substrate is processed with a greater depth

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